DONG et al.

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METHOD OF FORMING
GATE ELECTRODE IN SEMICONDUCTOR DEVICE

FIGS. 1 and 2

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FIG. 1

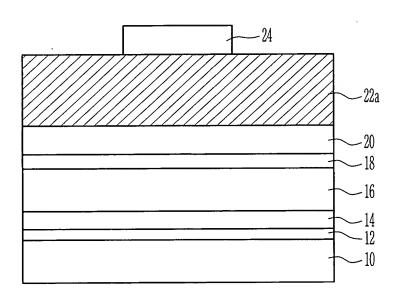
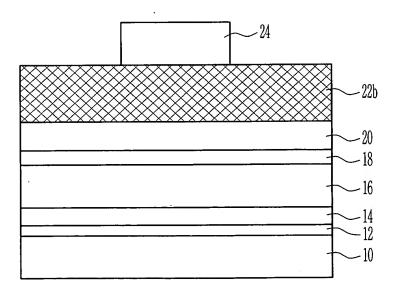


FIG. 2



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FIGS. 3 and 4

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